



PATENT

#19  
52404

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Dally et al. Examiner: Lane, J.  
Serial No.: 09/394,222 Group Art Unit: 2188  
Filed: September 13, 1999 Docket No.: STFD.073PA  
(S97-245)  
Title: System And Method For Re-Ordering Memory References In A Memory  
Control System To Speed Access To Memory Cells

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on May 10, 2004.

By: 

Jennifer L. Larson

SUPPLEMENTAL OFFICE ACTION RESPONSE

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Technology Center 2100

Dear Sir:

In response to the Advisory Action dated April 9, 2004, please consider the following remarks beginning on page 2.